TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL JUNCTION TYPE

2 S K 2 4 6

FOR CONSTANT CURRENT, IMPEDANCE

CONVERTER AND DC-AC HIGH INPUT

IMPEDANCE AMPLIFIER CIRCUIT APPLICATIONS

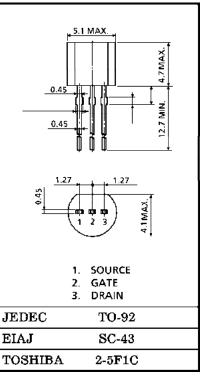
High Breakdown Voltage $: V_{GDS} = -50V$

High Input Impedance : $I_{GSS} = 1nA (Max.) (V_{GS} = -30V)$

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Gate-Drain Voltage	v_{GDS}	-50	V
Gate Current	$I_{\mathbf{G}}$	10	mA
Drain Power Dissipation	$P_{\mathbf{D}}$	300	mW
Junction Temperature	$\mathrm{T_{j}}$	125	°C
Storage Temperature Range	$\mathrm{T_{stg}}$	-55~125	°C

Unit in mm



Weight: 0.21g

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Cut-off Current	$I_{ m GSS}$	$V_{GS} = -30V, V_{DS} = 0$	_	_	-1.0	nA
Gate-Drain Breakdown Voltage	V _(BR) GDS	$V_{\rm DS} = 0$, $I_{\rm G} = -100 \mu {\rm A}$	-50	_	_	v
Drain Current	IDSS (Note)	$V_{DS} = 10V, V_{GS} = 0$	1,2	_	14	mA
Gate-Source Cut-off Voltage	V _{GS} (OFF)	$V_{DS} = 10V, I_D = 0.1 \mu A$	-0.7	_	-6.0	V
Forward Transfer Admittance	Y _{fs}	$V_{\rm DS} = 10 V, \ V_{\rm GS} = 0, \ f = 1 {\rm kHz}$	1.5	_	_	m\$
Input Capacitance	c_{iss}	$V_{DS} = 10V, V_{GS} = 0, f = 1MHz$	_	9.0	_	рF
Reverse Transfer Capacitance	C_{rss}	$V_{ m DG}$ =10V, $I_{ m D}$ =0, f=1MHz	_	2.5	_	р F

Note: IDSS Classification $Y: 1.2\sim 3.0 \text{mA}$, $GR: 2.6\sim 6.5 \text{mA}$, $BL: 6\sim 14 \text{mA}$

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