TOSHIBA 2SK2847

TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE (π -MOSIII)

2 S K 2 8 4 7

HIGH SPEED, HIGH CURRENT SWITCHING APPLICATIONS DC-DC CONVERTER AND MOTOR DRIVE APPLICATIONS

Low Drain-Source ON Resistance : $R_{DS(ON)} = 1.1\Omega$ (Typ.)

High Forward Transfer Admittance : $|Y_{fs}| = 7.0S$ (Typ.)

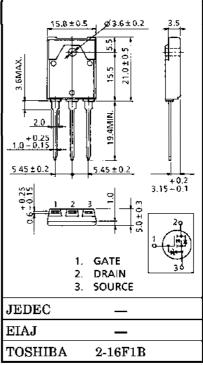
Low Leakage Current : $I_{DSS} = 100 \mu A$ (Max.) ($V_{DS} = 720 V$)

: $V_{th} = 2.0 \sim 4.0 \text{V (V}_{DS} = 10 \text{V, I}_{D} = 1 \text{mA})$ Enhancement-Mode

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERIS'	SYMBOL	RATING	UNIT	
Drain-Source Voltage	$v_{ m DSS}$	900	V	
Drain-Gate Voltage (RGS	$v_{ m DGR}$	900	V	
Gate-Source Voltage	v_{GSS}	±30	V	
Drain Current	DC	$I_{\mathbf{D}}$	8	A
Drain Current	Pulse	I_{DP}	24	A
Drain Power Dissipation	$P_{\mathbf{D}}$	85	W	
Single Pulse Avalanche	EAS	799	mJ	
Avalanche Current	I_{AR}	8	A	
Repetitive Avalanche En	EAR	8.5	mJ	
Channel Temperature	$\mathrm{T_{ch}}$	150	°C	
Storage Temperature Ran	$T_{ m stg}$	-55~150	$^{\circ}\mathrm{C}$	

INDUSTRIAL APPLICATIONS Unit in mm



Weight: 5.8g (Typ.)

THERMAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	$ m R_{th~(ch-c)}$	1.47	°C/W
Thermal Resistance, Channel to Ambient	Rth (ch-a)	41.6	°C/W

Note:

- * Repetitive rating; Pulse Width Limited by Max. junction temperature.
- ** V_{DD} =90V, Starting T_{ch} =25°C, L=22.9mH, R_{G} =25 Ω , I_{AR} =8A

This transistor is an electrostatic sensitive device. Please handle with caution.

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ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACT	ERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Gate Leakage C	urrent	$_{ m IGSS}$	$V_{GS} = \pm 30V, V_{DS} = 0V$	_	_	±10	μA	
Gate-Source Breakdown Voltage			$I_G = \pm 10 \mu A$, $V_{DS} = 0 V$	±30	_	_	v	
Drain Cut-off Cu	ırrent	$I_{ m DSS}$	$V_{DS} = 720V, V_{GS} = 0V$	_	_	100	μ A	
Drain-Source Breakdown Volt	age	V (BR) DSS	I _D =10mA, V _{GS} =0V	900	_	_	v	
Gate Threshold Voltage		$V_{\mathbf{th}}$	$V_{DS}=10V, I_{D}=1mA$	2.0	_	4.0	V	
Drain-Source ON Resistance		R _{DS} (ON)	$V_{GS}=10V$, $I_D=4A$	_	1.1	1.4	Ω	
Forward Transfer Admittance		$ Y_{fs} $	$V_{DS} = 15V, I_{D} = 4A$	3.0	7.0	_	S	
Input Capacitance		$\mathrm{c}_{\mathrm{iss}}$	$V_{DS} = 25V, V_{GS} = 0V, \\ f = 1MHz$	_	2040	_	pF	
Reverse Transfer Capacitance		$\mathrm{c}_{\mathrm{rss}}$		_	45	_		
Output Capacita	Output Capacitance			_	190	_		
Switching Time	Rise Time	t _r	$V_{GS} \stackrel{10V}{\underset{0V}{\text{ID}}} \stackrel{I_D=4A}{\underset{V_{OUT}}{\text{V}_{OUT}}} \\ - V_{IN}: t_r, t_f < 5 \text{ns}, & = 10 \mu \text{s}$		25	_		
	Turn-on Time	t _{on}		-	60	_		
	Fall Time	tf		1	20	_	ns	
	Turn-off Time	^t off		_	95	_		
Total Gate Charge (Gate-Source Plus Gate-Drain)		$\mathbf{Q_g}$	V _{DD} ≒400V, V _{GS} =10V,		58	_	- G	
Gate-Source Charge		Q_{gs}	$I_{D}=8A$	_	32	_	nC	
Gate-Drain ("Miller") Charge		$Q_{ m gd}$		_	26	_		

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	$I_{ m DR}$	_	_	_	8	A
Pulse Drain Reverse Current	$I_{ m DRP}$	_	_	_	24	Α
Diode Forward Voltage	$v_{ m DSF}$	$I_{DR}=8A$, $V_{GS}=0V$	_	_	-1.9	V
Reverse Recovery Time	t _{rr}	$I_{DR}=8A$, $V_{GS}=0V$	_	1650	_	ns
Reverse Recovery Charge	Q_{rr}	$dI_{DR}/dt = 100A/\mu s$	_	21	_	μC

MARKING

