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AO3401

P-Channel Enhancement Mode Field Effect Transistor

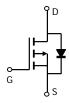
General Description

The AO3401 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications. Standard product AO3401 is Pb-free (meets ROHS & Sony 259 specifications). AO3401L is a Green Product ordering option. AO3401 and AO3401L are electrically identical.

Features

$$\begin{split} &V_{DS}\left(V\right) = -30V \\ &I_{D} = -4.2 \text{ A } \left(V_{GS} = -10V\right) \\ &R_{DS(ON)} < 50 \text{m} \Omega \left(V_{GS} = -10V\right) \\ &R_{DS(ON)} < 65 \text{m} \Omega \left(V_{GS} = -4.5V\right) \\ &R_{DS(ON)} < 120 \text{m} \Omega \left(V_{GS} = -2.5V\right) \end{split}$$





Absolute Maximum Ratings T _A =25°C unless otherwise noted								
Parameter		Symbol	Maximum	Units				
Drain-Source Voltage		V _{DS}	-30	V				
Gate-Source Voltage		V_{GS}	±12	V				
Continuous Drain	T _A =25°C		-4.2					
Current ^A	T _A =70°C	I _D	-3.5	Α				
Pulsed Drain Current ^B		I _{DM}	-30					
	T _A =25°C	В	1.4	W				
Power Dissipation A	T _A =70°C	$-P_D$	1	vv				
Junction and Storage Temperature Range		T _J , T _{STG}	-55 to 150	°C				

Thermal Characteristics								
Parameter		Symbol	Тур	Max	Units			
Maximum Junction-to-Ambient A	t ≤ 10s	В	65	90	°C/W			
Maximum Junction-to-Ambient A	Steady-State	y-State R _{θJA}		125	°C/W			
Maximum Junction-to-Lead ^C	Steady-State	$R_{\theta JL}$	43	60	°C/W			

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter Conditions		Min	Тур	Max	Units
STATIC F	PARAMETERS					
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D = -250 \mu A, V_{GS} = 0 V$	-30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-24V, V _{GS} =0V			-1	μА
		T _J =55°C	;		-5	μΛ
I_{GSS}	Gate-Body leakage current	V_{DS} =0V, V_{GS} =±12V			±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ $I_{D}=-250\mu A$	-0.7	-1	-1.3	V
$I_{D(ON)}$	On state drain current	V _{GS} =-4.5V, V _{DS} =-5V	-25			Α
R _{DS(ON)} S	Static Drain-Source On-Resistance	V _{GS} =-10V, I _D =-4.2A		42	50	mΩ
		T _J =125°C	;		75	11122
	Static Dialii-Source Off-Nesistatice	V _{GS} =-4.5V, I _D =-4A		53	65	mΩ
		V_{GS} =-2.5V, I_D =-1A		80	120	mΩ
g _{FS}	Forward Transconductance	V_{DS} =-5V, I_D =-5A		11		S
V_{SD}	Diode Forward Voltage	I _S =-1A,V _{GS} =0V		-0.75	-1	V
Is	Maximum Body-Diode Continuous Curr			-2.2	Α	
DYNAMIC	CPARAMETERS					
C _{iss}	Input Capacitance			954		pF
C _{oss}	Output Capacitance	V_{GS} =0V, V_{DS} =-15V, f=1MHz		115		pF
C _{rss}	Reverse Transfer Capacitance			77		pF
R_g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz		6		Ω
SWITCHI	NG PARAMETERS					
Q_g	Total Gate Charge			9.4		nC
Q_{gs}	Gate Source Charge	V_{GS} =-4.5V, V_{DS} =-15V, I_{D} =-4A		2		nC
Q_{gd}	Gate Drain Charge			3		nC
t _{D(on)}	Turn-On DelayTime			6.3		ns
t _r	Turn-On Rise Time	V_{GS} =-10V, V_{DS} =-15V, R_L =3.6 Ω ,		3.2		ns
t _{D(off)}	Turn-Off DelayTime	R_{GEN} =6 Ω		38.2		ns
t _f	Turn-Off Fall Time]		12		ns
t _{rr}	Body Diode Reverse Recovery Time	I _F =-4A, dI/dt=100A/μs		20.2		ns
Q _{rr}	Body Diode Reverse Recovery Charge	Recovery Charge I _F =-4A, dI/dt=100A/μs		11.2		nC

A: The value of $R_{\theta,JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C. The value in any given application depends on the user's specific board design. The current rating is based on the t≤ 10s thermal resistance rating.

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B: Repetitive rating, pulse width limited by junction temperature.

C. The R $_{\theta JA}$ is the sum of the thermal impedence from junction to lead R $_{\theta JL}$ and lead to ambient.

D. The static characteristics in Figures 1 to 6,12,14 are obtained using $80\,\mu s$ pulses, duty cycle 0.5% max.

E. These tests are performed with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C. The SOA curve provides a single pulse rating.