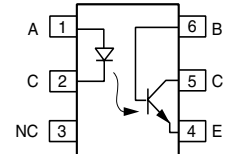
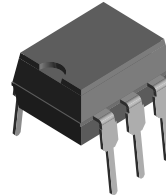




## Optocoupler, Phototransistor Output, With Base Connection

### Features

- Isolation Test Voltage 5300 V<sub>RMS</sub>
- Interfaces with common logic families
- Input-output coupling capacitance < 0.5 pF
- Industry Standard Dual-in line 6-pin package
- Lead-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC



1179004



### Agency Approvals

- Underwriters Laboratory File #E52744
- DIN EN 60747-5-2 (VDE0884)  
DIN EN 60747-5-5 pending  
Available with Option 1

### Applications

- AC mains detection
- Reed relay driving
- Switch mode power supply feedback
- Telephone ring detection
- Logic ground isolation
- Logic coupling with high frequency noise rejection

### Description

This data sheet presents five families of Vishay Industry Standard Single Channel Phototransistor Couplers. These families include the 4N35/ 4N36/ 4N37/ 4N38 couplers.

Each optocoupler consists of gallium arsenide infrared LED and a silicon NPN phototransistor.

These couplers are Underwriters Laboratories (UL) listed to comply with a 5300 V<sub>RMS</sub> isolation test voltage.

This isolation performance is accomplished through Vishay double molding isolation manufacturing process. Compliance to DIN EN 60747-5-2(VDE0884)/ DIN EN 60747-5-5 pending partial discharge isolation specification is available for these families by ordering option 1.

These isolation processes and the Vishay ISO9001 quality program results in the highest isolation performance available for a commercial plastic phototransistor optocoupler.

The devices are available in lead formed configuration suitable for surface mounting and are available either on tape and reel, or in standard tube shipping containers.

Note:

Designing with data sheet is cover in Application Note 45

### Order Information

Part	Remarks
4N35	CTR > 100 %, DIP-6
4N36	CTR > 100 %, DIP-6
4N37	CTR > 100 %, DIP-6
4N38	CTR > 20 %, DIP-6
4N35-X006	CTR > 100 %, DIP-6 400 mil (option 6)
4N35-X007	CTR > 100 %, SMD-6 (option 7)
4N35-X009	CTR > 100 %, SMD-6 (option 9)
4N36-X007	CTR > 100 %, SMD-6 (option 7)
4N36-X009	CTR > 100 %, SMD-6 (option 9)
4N37-X006	CTR > 100 %, DIP-6 400 mil (option 6)
4N37-X009	CTR > 100 %, SMD-6 (option 9)

For additional information on the available options refer to Option Information.

### Absolute Maximum Ratings

$T_{amb} = 25\text{ °C}$ , unless otherwise specified

Stresses in excess of the absolute Maximum Ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute Maximum Rating for extended periods of the time can adversely affect reliability.

### Input

Parameter	Test condition	Symbol	Value	Unit
Reverse voltage		$V_R$	6.0	V
Forward current		$I_F$	60	mA
Surge current	$\leq 10\ \mu\text{s}$	$I_{FSM}$	2.5	A
Power dissipation		$P_{diss}$	100	mW

### Output

Parameter	Test condition	Symbol	Value	Unit
Collector-emitter breakdown voltage		$V_{CEO}$	70	V
Emitter-base breakdown voltage		$V_{EBO}$	7.0	V
Collector current		$I_C$	50	mA
	$(t \leq 1.0\ \text{ms})$	$I_C$	100	mA
Power dissipation		$P_{diss}$	150	mW

### Coupler

Parameter	Test condition	Symbol	Value	Unit
Isolation test voltage		$V_{ISO}$	5300	$V_{RMS}$
Creepage			$\geq 7.0$	mm
Clearance			$\geq 7.0$	mm
Isolation thickness between emitter and detector			$\geq 0.4$	mm
Comparative tracking index per DIN IEC 112/VDE0303, part 1			175	
Isolation resistance	$V_{IO} = 500\ \text{V}, T_{amb} = 25\text{ °C}$	$R_{IO}$	$10^{12}$	$\Omega$
	$V_{IO} = 500\ \text{V}, T_{amb} = 100\text{ °C}$	$R_{IO}$	$10^{11}$	$\Omega$
Storage temperature		$T_{stg}$	- 55 to + 150	$^{\circ}\text{C}$
Operating temperature		$T_{amb}$	- 55 to + 100	$^{\circ}\text{C}$
Junction temperature		$T_j$	100	$^{\circ}\text{C}$
Soldering temperature	max. 10 s dip soldering: distance to seating plane $\geq 1.5\ \text{mm}$	$T_{sld}$	260	$^{\circ}\text{C}$



### Electrical Characteristics

T<sub>amb</sub> = 25 °C, unless otherwise specified

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

### Input

Parameter	Test condition	Symbol	Min	Typ.	Max	Unit
Forward voltage <sup>1)</sup>	I <sub>F</sub> = 10 mA	V <sub>F</sub>		1.3	1.5	V
	I <sub>F</sub> = 10 mA, T <sub>amb</sub> = - 55 °C	V <sub>F</sub>	0.9	1.3	1.7	V
Reverse current <sup>1)</sup>	V <sub>R</sub> = 6.0 V	I <sub>R</sub>		0.1	10	μA
Capacitance	V <sub>R</sub> = 0, f = 1.0 MHz	C <sub>O</sub>		25		pF

<sup>1)</sup> Indicates JEDEC registered value

### Output

Parameter	Test condition	Part	Symbol	Min	Typ.	Max	Unit
Collector-emitter breakdown voltage <sup>1)</sup>	I <sub>C</sub> = 1.0 mA	4N35	BV <sub>CEO</sub>	30			V
		4N36	BV <sub>CEO</sub>	30			V
		4N37	BV <sub>CEO</sub>	30			V
		4N38	BV <sub>CEO</sub>	80			V
Emitter-collector breakdown voltage <sup>1)</sup>	I <sub>E</sub> = 100 μA		BV <sub>ECO</sub>	7.0			V
Collector-base breakdown voltage <sup>1)</sup>	I <sub>C</sub> = 100 μA, I <sub>B</sub> = 1.0 μA	4N35	BV <sub>CBO</sub>	70			V
		4N36	BV <sub>CBO</sub>	70			V
		4N37	BV <sub>CBO</sub>	70			V
		4N38	BV <sub>CBO</sub>	80			V
Collector-emitter leakage current <sup>1)</sup>	V <sub>CE</sub> = 10 V, I <sub>F</sub> = 0	4N35	I <sub>CEO</sub>		5.0	50	nA
		4N36	I <sub>CEO</sub>		5.0	50	nA
	V <sub>CE</sub> = 10 V, I <sub>F</sub> = 0	4N37	I <sub>CEO</sub>		5.0	50	nA
	V <sub>CE</sub> = 30 V, I <sub>F</sub> = 0, T <sub>amb</sub> = 100 °C	4N38	I <sub>CEO</sub>			50	nA
		4N35	I <sub>CEO</sub>			500	μA
		4N36	I <sub>CEO</sub>			500	μA
	V <sub>CE</sub> = 60 V, I <sub>F</sub> = 0, T <sub>amb</sub> = 100 °C	4N37	I <sub>CEO</sub>			500	μA
V <sub>CE</sub> = 60 V, I <sub>F</sub> = 0, T <sub>amb</sub> = 100 °C	4N38	I <sub>CEO</sub>		6.0		μA	
Collector-emitter capacitance	V <sub>CE</sub> = 0		C <sub>CE</sub>		6.0		pF

<sup>1)</sup> Indicates JEDEC registered value

### Coupler

Parameter	Test condition	Symbol	Min	Typ.	Max	Unit
Resistance, input to output <sup>1)</sup>	V <sub>IO</sub> = 500 V	R <sub>IO</sub>	10 <sup>11</sup>			Ω
Capacitance (input-output)	f = 1.0 MHz	C <sub>IO</sub>		0.5		pF

<sup>1)</sup> Indicates JEDEC registered value