

Silicon NPN Power Transistors

2SC4370

DESCRIPTION

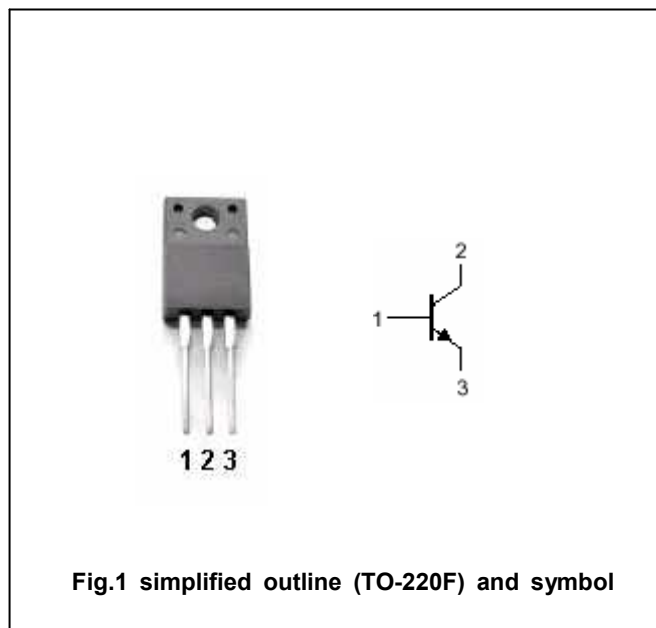
- With TO-220F package
- Complement to type 2SA1659
- High transition frequency

APPLICATIONS

- High voltage applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

Absolute maximum ratings ($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	160	V
V_{CEO}	Collector-emitter voltage	Open base	160	V
V_{EBO}	Emitter-base voltage	Open collector	5	V
I_C	Collector current		1.5	A
I_B	Base current		0.15	A
P_C	Collector dissipation	$T_C=25^\circ\text{C}$	20	W
T_j	Junction temperature		150	$^\circ\text{C}$
T_{stg}	Storage temperature		-55~150	$^\circ\text{C}$

Silicon NPN Power Transistors**2SC4370****CHARACTERISTICS****T_j=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =10mA ; I _B =0	160			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =0.5A; I _B =50mA			1.5	V
V _{BE}	Base-emitter on voltage	I _C =0.5A; V _{CE} =5V			1.0	V
I _{CBO}	Collector cut-off current	V _{CB} =160V; I _E =0			1.0	μA
I _{EBO}	Emitter cut-off current	V _{EB} =5V; I _C =0			1.0	μA
h _{FE}	DC current gain	I _C =0.1A ; V _{CE} =5V	70		240	
f _T	Transition frequency	I _C =0.1A ; V _{CE} =10V		100		MHz
C _{OB}	Collector output capacitance	f=1MHz; V _{CB} =10V		25		pF

◆ h_{FE} Classifications

O	Y
70-140	120-240

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PACKAGE OUTLINE

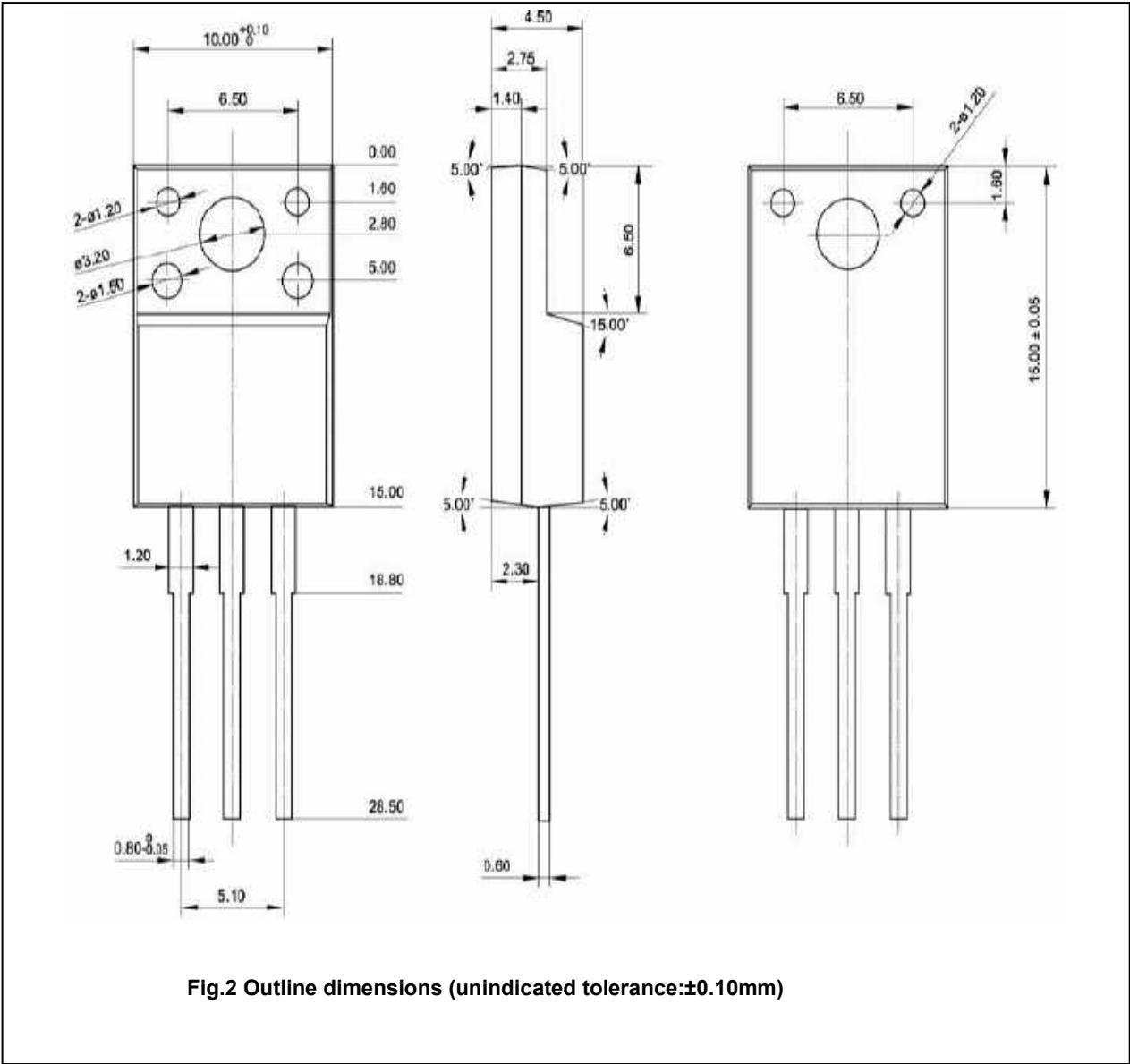


Fig.2 Outline dimensions (unindicated tolerance:±0.10mm)