

Silicon NPN Power Transistors

2SD1409

DESCRIPTION

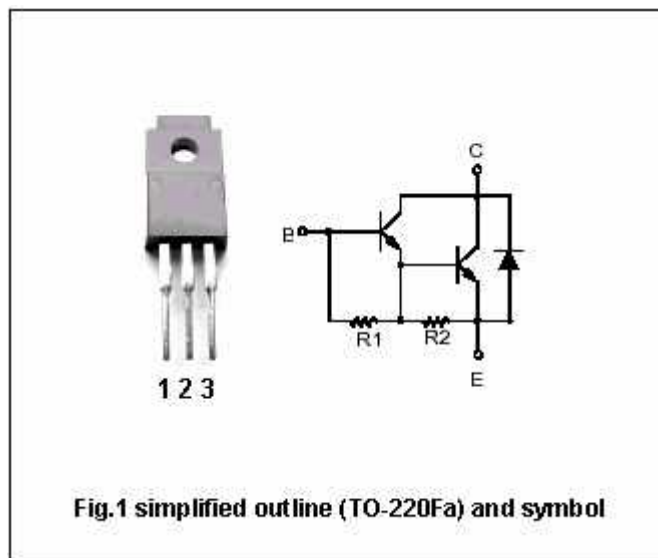
- With TO-220Fa package
- High DC current gain
- DARLINGTON

APPLICATIONS

- Igniter applications
- High voltage switching applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

Absolute maximum ratings($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	600	V
V_{CEO}	Collector -emitter voltage	Open base	400	V
V_{EBO}	Emitter-base voltage	Open collector	5	V
I_C	Collector current		6	A
I_B	Base current		1	A
P_C	Collector power dissipation	$T_C=25$	25	W
		$T_a=25$	2.0	
T_j	Junction temperature		150	
T_{stg}	Storage temperature		-55~150	

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CHARACTERISTICS

Tj=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)CEO}$	Collector-emitter breakdown voltage	$I_C=10mA; I_B=0$	400			V
V_{CEsat}	Collector-emitter saturation voltage	$I_C=4A; I_B=0.04A$			2.0	V
V_{BEsat}	Base-emitter saturation voltage	$I_C=4A; I_B=0.04A$			2.5	V
V_{ECF}	Emitter-collector diode forward voltage	$I_E=4A; I_B=0$			3.0	V
I_{CBO}	Collector cut-off current	$V_{CB}=600V; I_E=0$			0.5	mA
I_{EBO}	Emitter cut-off current	$V_{EB}=5V; I_C=0$			3	mA
h_{FE-1}	DC current gain	$I_C=2A; V_{CE}=2V$	600			
h_{FE-2}	DC current gain	$I_C=4A; V_{CE}=2V$	100			
C_{OB}	Collector output capacitance	$f=1MHz; V_{CB}=50V; I_E=0$		35		pF

Switching times

t_{on}	Turn-on time	$I_{B1}=-I_{B2}=0.04A$ $V_{CC}=100V, R_L=25\Omega$		1		μs
t_{stg}	Storage time			8		μs
t_f	Fall time			5		μs

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PACKAGE OUTLINE

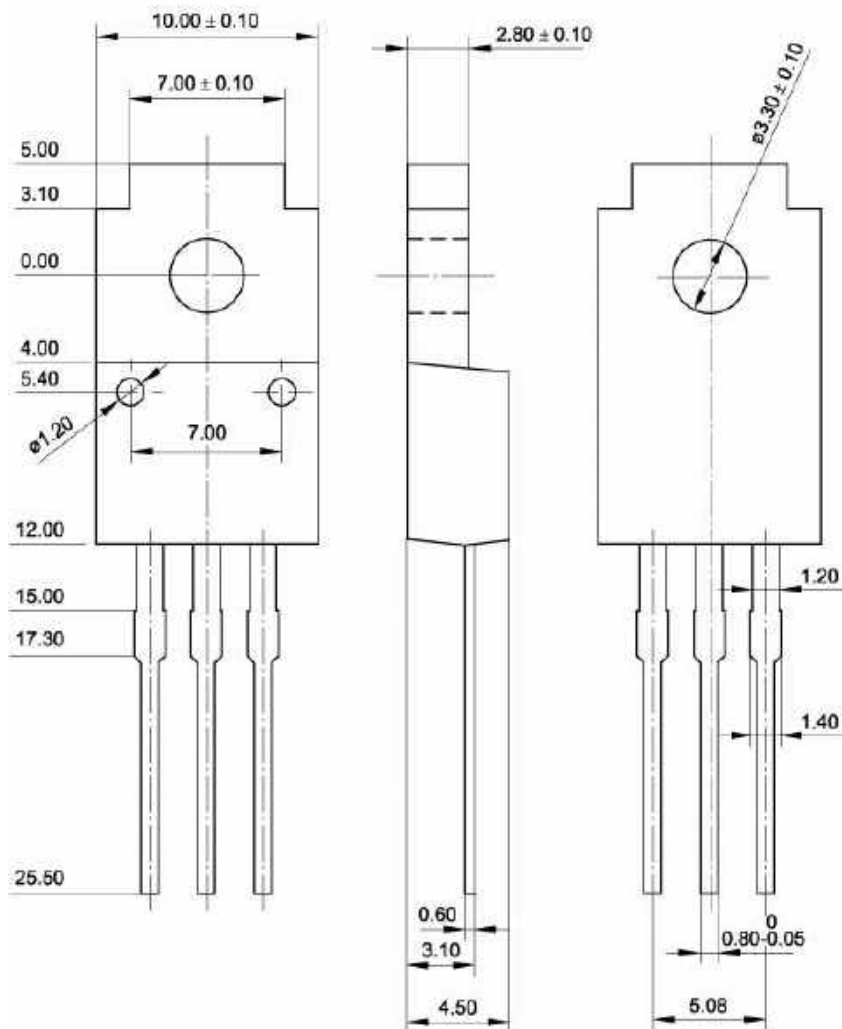


Fig.2 outline dimensions (unindicated tolerance:±0.15 mm)