

FEATURES

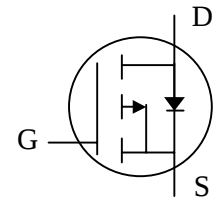
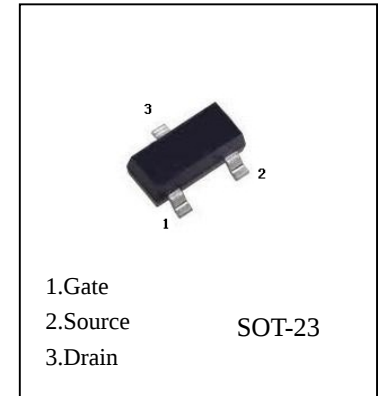
- High dense cell design for extremely low $R_{DS(ON)}$
- Rugged and reliable
- Case Material: Molded Plastic.

Absolute Maximum Ratings (TA=25°C, unless otherwise noted)

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V _{DS}	-30	V
Gate-source Voltage	V _{GS}	±12	V
Drain Current (Continuous)	I _D	-3.2	A
Drain Current (Pulsed) ^a	I _{DM}	-10	A
Total Power Dissipation @TA=25oC	P _D	1.38	W
Operating Junction and Storage Temperature Range	T _j , T _{stg}	-55 to +150	°C
Thermal Resistance Junction to Ambient (PCB mounted) ^b	R _{θJA}	90	°C/W

SI2305

P-Channel MOSFET



Electrical Characteristics (TA=25°C, unless otherwise noted)

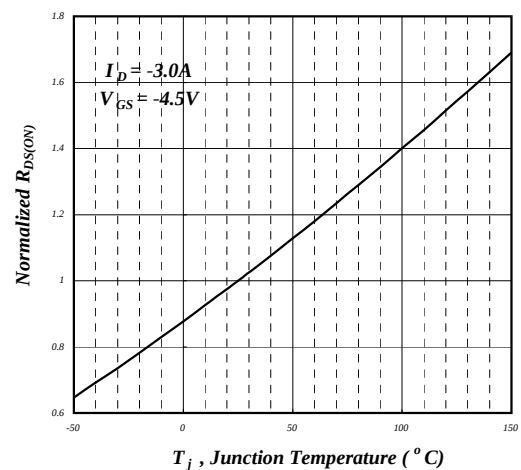
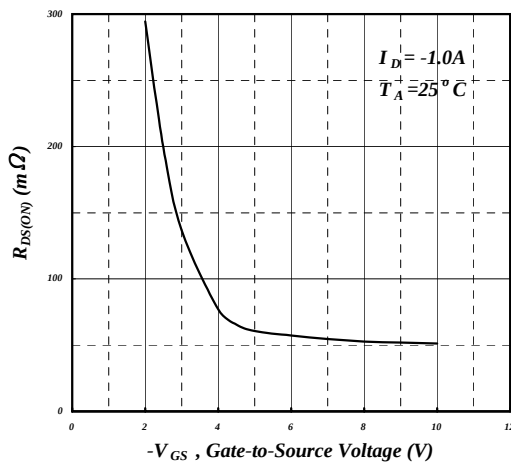
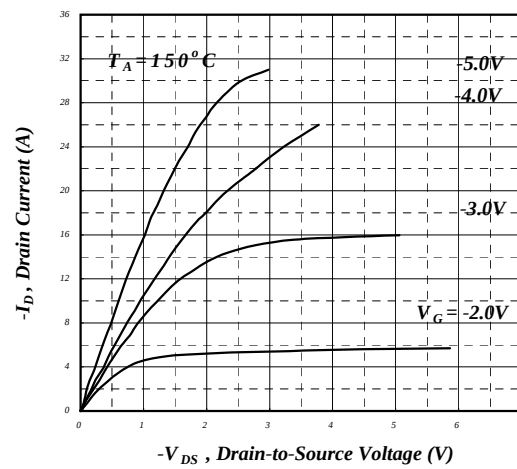
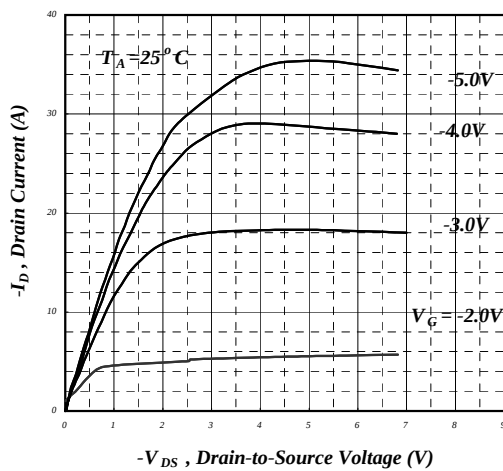
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
B _V D _{SS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-30	-	-	V
ΔB _V D _{SS} /ΔT _j	Breakdown Voltage Temperature Coefficient	Reference to 25 °C, I _D =-1mA	-	-0.1	-	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-10V, I _D =-3.2A	-	-	60	mΩ
		V _{GS} =-4.5V, I _D =-3.0A	-	-	80	mΩ
		V _{GS} =-2.5V, I _D =-2.0A	-	-	150	mΩ
		V _{GS} =-1.8V, I _D =-1.0A	-	-	250	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250uA	-0.5	-	-1.2	V
g _{fs}	Forward Transconductance	V _{DS} =-5V, I _D =-3.0A	-	9	-	S
I _{DSS}	Drain-Source Leakage Current (T _j =25°C)	V _{DS} =-30V, V _{GS} =0V	-	-	-1	uA
	Drain-Source Leakage Current (T _j =70°C)	V _{DS} =-24V, V _{GS} =0V	-	-	-25	uA
I _{GSS}	Gate-Source Leakage	V _{GS} = ± 12V	-	-	±100	nA
Q _g	Total Gate Charge	I _D =-3.2A	-	10	18	nC
Q _{gs}	Gate-Source Charge	V _{DS} =-24V	-	1.8	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =-4.5V	-	3.6	-	nC
t _{d(on)}	Turn-on Delay Time	V _{DS} =-15V	-	7	-	ns
t _r	Rise Time	I _D =-3.2A	-	15	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =3.3Ω, V _{GS} =-10V	-	21	-	ns
t _f	Fall Time	R _D =4.6Ω	-	15	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V V _{DS} =-25V f=1.0MHz	-	735	1325	pF
C _{oss}	Output Capacitance		-	100	-	pF
C _{rss}	Reverse Transfer Capacitance		-	80	-	pF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage	$I_S = -1.2A, V_{GS} = 0V$	-	-	-1.2	V
t_{rr}	Reverse Recovery Time	$I_S = -3.2A, V_{GS} = 0V,$ $dI/dt = 100A/\mu s$	-	24	-	ns
Q_{rr}	Reverse Recovery Charge		-	19	-	nC

a. Repetitive Rating : Pulse width limited by maximum junction temperature.

b. Surface Mounted on FR4 Board, $t < 10$ sec.

SI2305 Typical Characteristics



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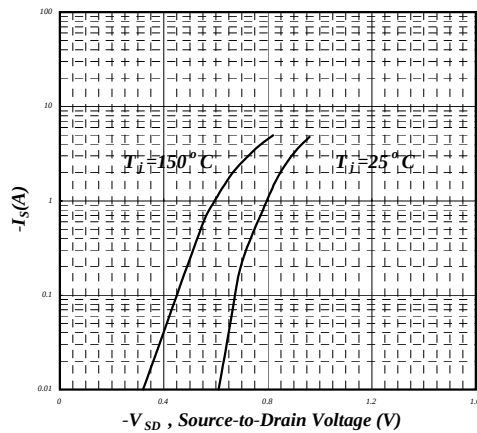


Fig 5. Forward Characteristic of Reverse Diode

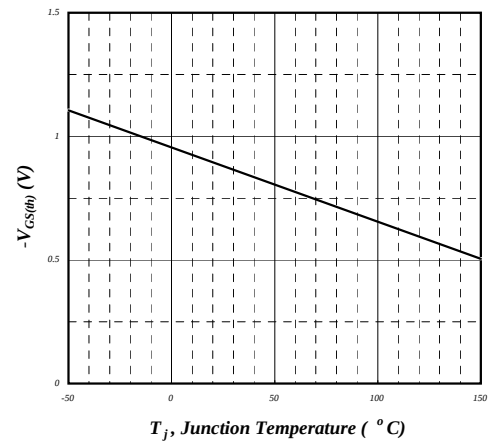


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

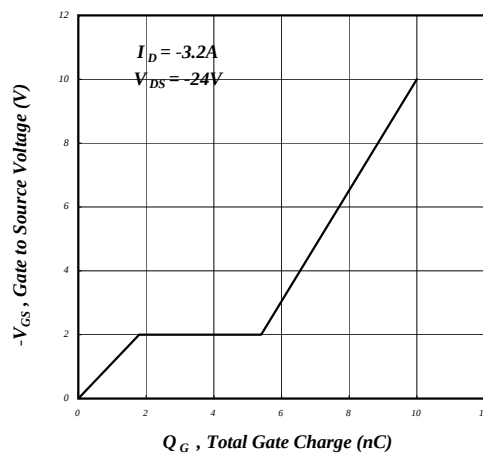


Fig 7. Gate Charge Characteristics

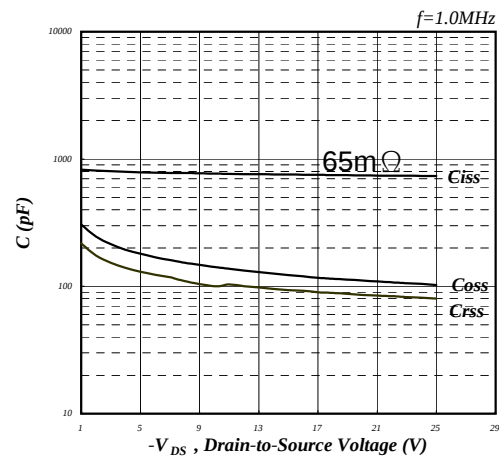


Fig 8. Typical Capacitance Characteristics

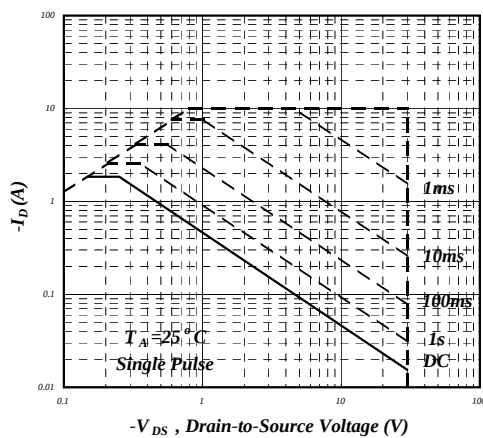


Fig 9. Maximum Safe Operating Area

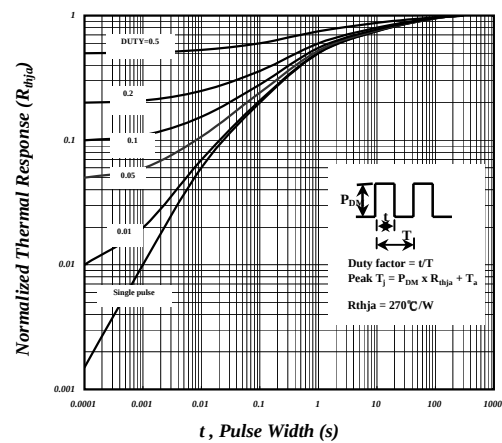


Fig 10. Effective Transient Thermal Impedance